ST 2SB772S

PNP Silicon Epitaxial Transistor

Medium Power Low Voltage Transistor

The transistor is subdivided into three groups Q, P and E, according to its DC current gain.

On special request, these transistors can be manufactured in different pin configurations.



1. Emitter 2. Collector 3. Base TO-92 Plastic Package Weight approx. 0.19g

Absolute Maximum Ratings (T_a = 25 °C)

Parameter	Symbol	Value	Unit	
Collector-Base Voltage	-V _{CBO}	40	V	
Collector-Emitter Voltage	-V _{CEO}	30	V	
Emitter-Base Voltage	-V _{EBO}	5	V	
Collector Current	-I _C	3	A	
Peak Collector Current	-I _{CM}	7	А	
Base Current	-I _B	600	mA	
Collector Dissipation	P _{tot}	500	mW	
Junction Temperature	Tj	150	°C	
Storage Temperature Range	T _S	- 55 to + 150	°C	

Characteristics ($T_a = 25 \circ C$)

Parameter		Symbol	Min.	Тур.	Max.	Unit
DC Current Gain						
at $-V_{CE} = 2 V$, $-I_C = 1 A$ Current Gain Group	Q	h _{FE}	100	-	200	-
	P E	h _{FE}	160	-	320	-
at $-V_{CE} = 2 V$, $-I_{C} = 20 mA$	C	h _{FE} h _{FE}	200 30	-	400 -	-
Collector Cutoff Current at $-V_{CB} = 30 \text{ V}$		-I _{CBO}	-	-	1	μA
Emitter Cutoff Current at $-V_{EB} = 3 V$		-I _{EBO}	-	-	1	μA
Collector-Emitter Saturation Voltage at $-I_C = 2 A$, $-I_B = 200 mA$		-V _{CE(sat)}	-	-	0.5	V
Base-Emitter Saturation Voltage at $-I_{C}$ = 2 A, $-I_{B}$ = 200 mA		-V _{BE(sat)}	-	-	2	V
Current Gain Bandwidth Product at $-V_{CE} = 5 V$, $-I_C = 0.1 A$		f⊤	-	80	-	MHz
Output Capacitance at $-V_{CB} = 10 \text{ V}, \text{ f} = 1 \text{ MHz}$		C _{ob}	-	45	-	pF

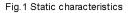


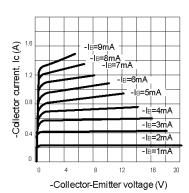
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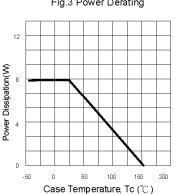


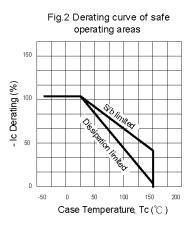
Dated : 04/10/2006

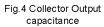












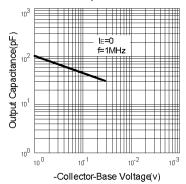


Fig.5 Current gainbandwidth product 10³ Ourrent gain-bandwidth product, f⊤(MHz) VCE=5V 10² =8mA 10 10⁰ 10⁻² 10-1 10 0 10

Fig.6 Safe Operating Area

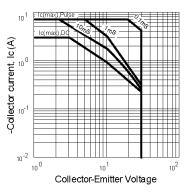
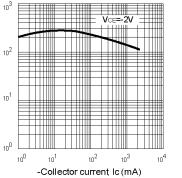
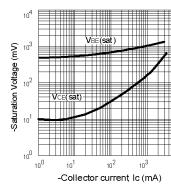


Fig.8 Saturation Voltage



DC current Gain, HFE

Collector current, Ic (A) Fig.7 DC current gain





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